

## HL957x Series 4-Way Power Dividers (DC to 67 GHz)

### Features and Technical Specifications<sup>1</sup> (HL9577 shown)

#### PRODUCT SUMMARY

The HL957x series are ultra-broadband 12 dB 4-way power dividers that provide outstanding amplitude- and phase-symmetrical power division from DC to beyond 65 GHz.

This product is designed using multiple three-resistor networks resulting in outputs that are nominally attenuated to 12 dB, and all ports are impedance-matched to 50 Ohms when all ports are terminated.

They are suitable for use as a clock splitter and in 112 Gbps PAM4 communications systems, high-speed analog-to-digital conversion, frequency response testing for differential devices, and many other applications.

When used alongside Hyperlabs ultra-broadband baluns, these devices can be used to drive multiple differential high-speed analog-to-digital converters.

#### DEPLOYMENT NOTES

The ports of the HL957x series are symmetrical and the device can be used in any direction.

#### MODELS & OPTIONS

The following models are available:

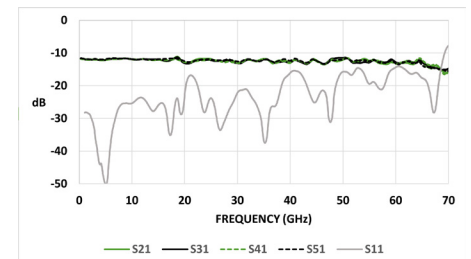
- HL9572**, 26.5 GHz
- HL9574**, 40 GHz
- HL9455**, 50 GHz
- HL9577**, 67 GHz

Bandwidth	DC to 67 GHz
Insertion Loss	12 dB
Amplitude Match	$\pm 0.5$ dB See Fig. 1
Phase Match	$\pm 4^\circ$ , $f = 20$ GHz $\pm 8^\circ$ , $f = 40$ GHz See Fig. 4
Return Loss	$> 15$ dB, $f \leq 45$ GHz $> 10$ dB, $f > 45$ GHz See Fig. 2
Rise Time	5 ps
Insertion (Group) Delay	198 ps, all ports See Fig. 3
Max Input Power	+33 dBm
Impedance	$50 \Omega \pm 5\%$
Connectors	1.85 mm, 5x jack/female
Dimensions (L x W x H)	1.575" x 1.79" x 0.40" 40 x 45.5 x 10.16 mm See Fig. 9
Temperature Limits	-40° to +70° C, operating
RoHS Compliant	Yes, assembled with lead-free solder
REACH Compliant	Yes
Warranty	1 year, see website

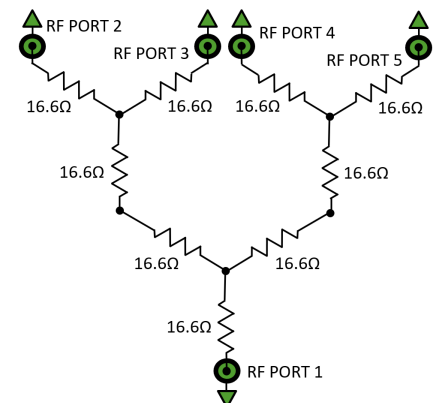
NOTE 1 - Unless otherwise noted, the specifications in this table are typical for Model Number HL9577. Full specifications for this and related models are available on Page 2 of this datasheet.



HL9577, standard configuration shown



Typical HL9577 Insertion and Return Loss



HL957x Schematic and Port Assignments

## HL957x Full Specifications

Parameter	HL9572	HL9574	HL9575	HL9577	Comments
Upper Frequency Limit	26.5 GHz	40 GHz	50 GHz	67 GHz, typical 64 GHz, minimum	3 dB guaranteed, relative to nominal insertion loss
Lower Frequency Limit	DC				
Insertion Loss (DC)	12 dB				
Insertion Loss (AC) <i>See Fig. 1</i>	12 ± 0.5 dB ripple				Typical, nominal
Return Loss <i>See Fig. 2</i>	> 24 dB, f = 20 GHz	> 20 dB, f ≤ 30 GHz	> 20 dB, f ≤ 30 GHz > 15 dB, f > 30 GHz	> 15 dB, f ≤ 45 GHz > 10 dB, f > 45 GHz	Typical
Amplitude Match <i>See Fig. 1</i>	± 0.5 dB				Typical, between all ports
Phase Match <i>See Fig. 4</i>	± 4°, f = 20 GHz	± 4°, f = 20 GHz ± 8, f = 40 GHz	± 4°, f = 20 GHz ± 8, f = 40 GHz	± 4°, f = 20 GHz ± 8, f = 40 GHz	Typical, between all ports
Rise Time	17.5 ps	8.75 ps	7 ps	5.2 ps	Typical
Insertion (Group) Delay <i>See Fig. 3</i>	198 ps				Typical, all ports
Max Input Power	+33 dBm				
Impedance	50 Ω ± 5%				All ports
Connectors	SMA, 3x jack/female	2.92 mm, 3x jack/female	2.4 mm, 3x jack/female	1.85 mm, 3x jack/female	Plug/male connectors available upon request
Length and Width	1.575" x 1.79" 40 x 45.5 mm				
Height	0.40" 10.16 mm				
Weight	14 g (0.49 oz.)				
Operating Temperature	-40° to +70° C				Case temperature
RoHS Compliant	Yes, assembled with lead-free solder				
REACH Compliant	Yes				
Warranty	1 year, repair or replacement; see website for details				

## HL957x Insertion and Return Loss

The HL9577 is matched to 50  $\Omega$  on all ports. Port 1 is specified with a dot on the label, and Ports 2-5 are arranged clockwise from Port 1.

Figure 1 shows the HL9577 insertion loss and amplitude match on Ports 2-4 to 70 GHz. Figure 2 shows return loss on all three ports of the same device to 70 GHz. Other models show similar performance within their respective specified bandwidths.

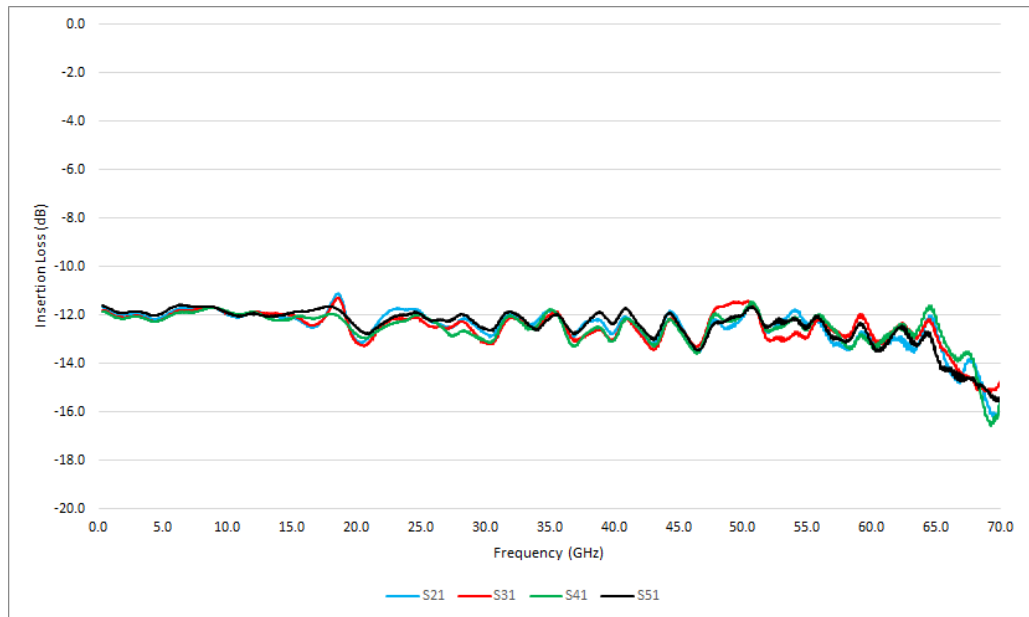


Figure 1: HL9577 Insertion Loss

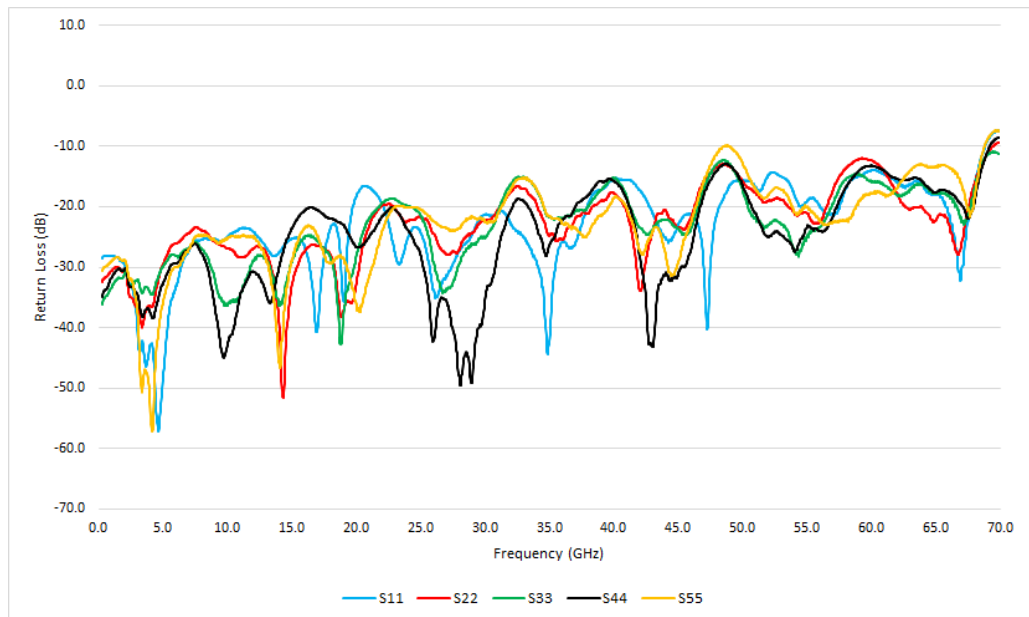


Figure 2: HL9577 Return Loss

## HL957x Group Delay and Phase Match

Figure 3 shows the typical group delay of an HL9577 on Ports 2 and 3. The average slope of the phase mismatch, shown in Figure 4, is equal to the group delay mismatch. Other models show similar performance within respective specified bandwidths.

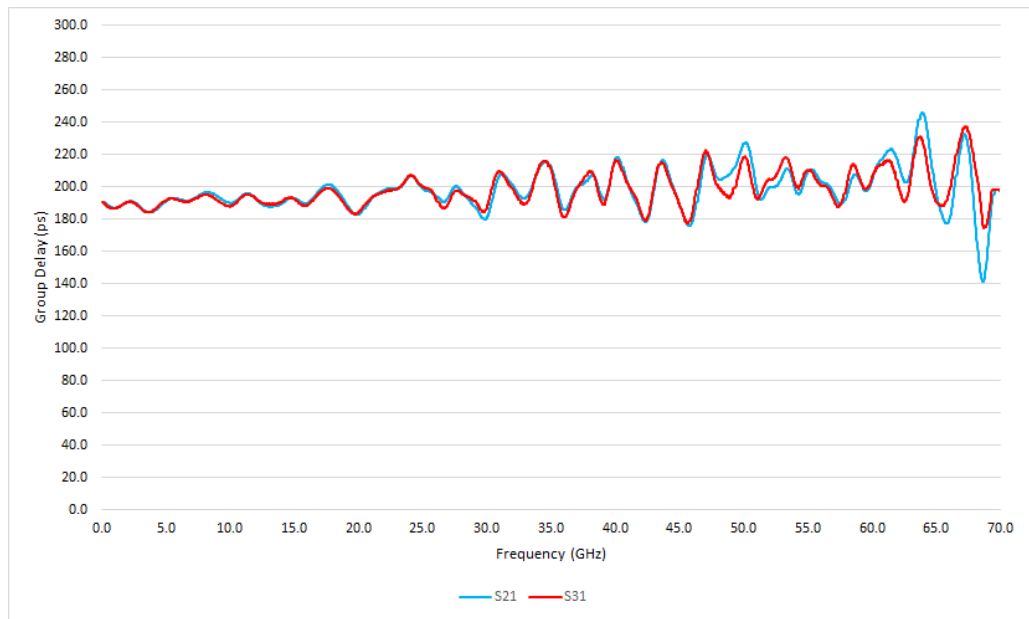


Figure 3: HL9577 Group Delay

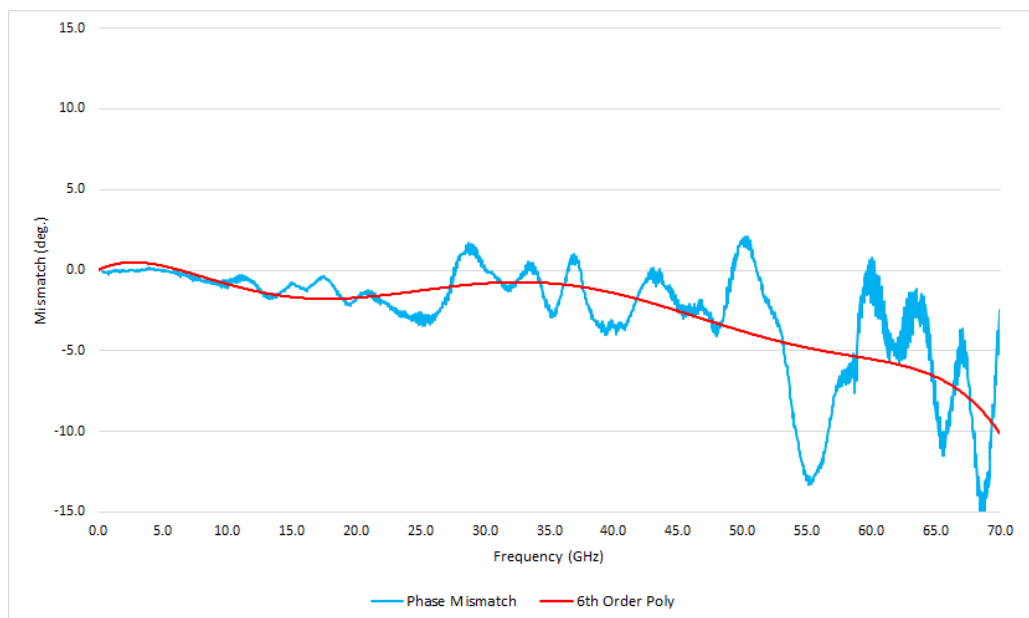


Figure 4: HL9577 Phase Mismatch

### HL957x Dimensional Drawing

Figure 9 shows a mechanical drawing of an HL9577. Unless otherwise noted, all units are shown in inches. Other models vary in length and width based on connectors.

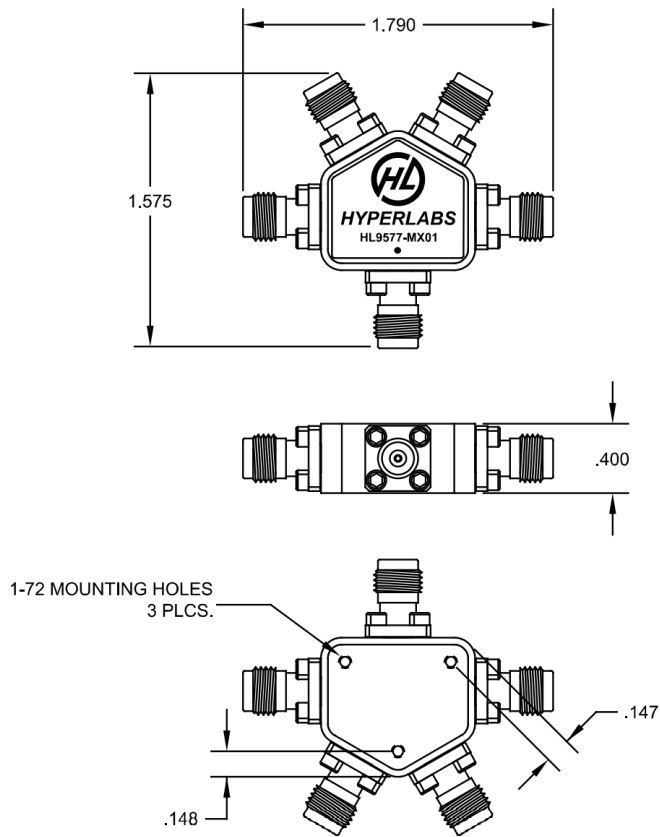


Figure 9: HL9577 Mechanical Drawing